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by B Razavi - 1996 - Cited by 491 - Related articles
The choice of **number** of stages in a ring **oscillator** to minimize the phase noise has often
been ... and each stage must have a phase shift of 180 , with **90** contributed by each zero
and the ... **random** Gaussian noise can be expressed as a Fourier series of .... **degree** in
electrical engineering from Tehran (Sharif) ...
www.ee.ucla.edu/~brweb/papers/Journals/BRMar96.pdf - Similar

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random-frequency access and the rapid tunability provide promising THz-wave .... 10 × 5
mm (base × thickness) with angles of 50, 40, and **90 degrees**. ...
www.opticsinfobase.org/oe/viewmedia.cfm?id=98598&seq=0

(WO/2007/131085) MICROELECTROMECHANICAL MULTI-STAGE **OSCILLATOR**
[0020] In the drawings, the same reference **numbers** identify identical or ... read only
memory (ROM), **random** access memory (RAM), reprogrammable PROM, ... Pierce
oscillator has approximately **90 degrees** of phase lag at the input node, ...
www.wipo.int/pctdb/ja/wo.jsp?WO=2007131085&IA... - Cached

Numerical Stability Analysis of a Forced Two-D.O.F. **Oscillator** ...
by Z Szabó - 2007
All of the above-mentioned works dealed with one-**degree**-of-freedom models. ...
determined from a **sample** function by averaging in time (see detail in). .... For this
purpose, **random numbers** are computed using deterministic ... J., 1983, "A Periodically
Forced Piecewise Linear **Oscillator**," J. Sound Vib., **90**(1), pp. ...
link.aip.org/link/?JCNDDM/2/211/1

1 **OSCILLATION** MECHANICS OF THE HUMAN LUNG PERIPHERY IN ASTHMA ...
by DA Kaminsky - 2004 - Cited by 19 - Related articles
Jun 25, 2004 ... **random** search to minimize the peak-to-peak amplitude excursion of ....
measurement with the bronchoscope bent at **90 degrees** to mimic ..... to other factors such
as technique (whole-lung vs. peripheral **oscillation**), **sample** size (21 vs. ... made a
number of assumptions about exactly how wall stiffness ...
jap.physiology.org/cgi/reprint/00300.2004v1.pdf

[PDF] A new phase-shifterless beam-scanning technique using arrays of ...
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by P Liao - 1993 - Cited by 151 - Related articles
Ad < +**90**°, and is independent of the **number** of **oscillators** in the array. ..... -30 0 30.
Angle, **Degrees**. (b). -**90** -60 -30 0 30. Angle, **Degrees** .... ena in lattices of coupled
nonlinear **oscillators** with **random** intrinsic ...
my.ece.ucsb.edu/.../34%20-%20MTT%20Oct%201993%20Scanning.pdf

kyma•tweaky: Share Sounds
VCF1.kym by ChristiaanGelauff: VCF is FM-like **oscillator**. ... HilbertExamples.zip by
DavidMcClain: Hilbert transform Sounds (implements **90-degree** phase shift). ... preset
random pattern generator - change the **random number** seeds! ...
BeatChopSequencer.kym: by PhilCurtis: 2 parallel sequencers, with **sample** loop ...
www.symbolicsound.com/cgi-bin/bin/view/Share/Sounds

Radiofrequency wave transmission circuit using arbitrary waveform ...

by H Habara - 2006
Nov 7, 2006 ... An NMR apparatus has a circuit adapted to improve the degree freedom ... a
phase shifter for **90 degree** shifting the phase of the .... The output waveform of the local
oscillator LOSC, for example, ... where random (k) represents a **random number** taking a
real number between 0 and 1 at seed integer k. ...
www.freepatentsonline.com/7132830.html

Bit storage and bit flip operations in an electromechanical ...
by I Mahboob - 2008 - Cited by 16 - Related articles
Apr 13, 2008 ... 1/Q is the damping in the mechanical **oscillator**, alpha .... phases have now
become **random**. The fidelity of the bit flip ... VT by counting the **number** of errors in a bit
flip sequence (Fig. ... The **sample** then underwent a rapid thermal anneal in an H2 ambient
for 60 s to alloy the AuGeNi to the 2DES. ...
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Jul 31, 2008 ... 4.2 Monte Carlo Analysis and **Random Number** Generator ... **Sample**
correlation matrix for chosen process parameters ..... percentage of total variance is
relatively high (say **90**%) then the original data can be ..... determine if some parameters
have high **degree** of intercorrelation, m essence producmg ...
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The Design and Analysis of a. True **Random Number** Generator in an **FPGA**. Paul
Kohlbrenner ... **Sample**. Reset. RandBitOu t. BitReady. ReadAck. Ring **Oscillator** ...
ite.gmu.edu/~kgal/publications/conferences/GMU_FPGA_2004_slides.pdf

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less effective in the production of truly **random numbers**. A malicious party could
manipulate or even **sample** the source of noise of such a generator and thus ...
www.crypto.wpi.edu/Publications/Documents/MQP_jitter_1.pdf - Similar

High speed true **random number** generator based on open loop ...
by JL Danger - 2009 - Cited by 6 - Related articles
Pseudo **Random Number** Generators (prng) can suffice for most applications but ... The
simplest method to generate trng in a cmos circuit is to **sample** the thermal noise. ... The
oscillator structures are easily implementable in **FPGAs**. ...
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Aside from the trade-off between **number** of pipeline stages and ... properties such as
channel width, as well as **random** fluctuations in electrical properties ... Figure 2: Ring
Oscillator Frequency Sampled on the Virtex II **FPGA** ... of values for a small **sample** size,
the t distribution should fit the data obtained. ...
citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.75.692&rep...

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by B Sunar - Related articles
Before we can **sample** we want to make sure the spectrum is populated with jitter. .... A
large increase in the **number** of **oscillators** is required to ... Evaluation of RNGs Using
FPGAs, May 2004. WPI, MQP Report. ...
www.ipam.ucla.edu/publications/scws4/scws4_6650.pdf

electro-music.com :: View topic - **FPGA** Sine Synth
4 posts - 3 authors - Last post: Dec 14, 2009
The attached MP3 **sample** uses only 16 of the 32 **oscillators**. The **oscillators** are set in
frequency as prime **number** harmonics from 1 to 47. ... adjustments to a few things.
specifically emphasizing the noise/**random** aspect ...
electro-music.com/forum/post-286050.html - Cached

Clock synthesis using polyphase numerically controlled **oscillator** ...
Extract MSBs circuit 102 can, for example, include an N **number** of M-to-1 ..... **FPGA** 500
also includes a distributed memory structure including **random** access ...
www.patentstorm.us/patents/7598790/description.html

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... either from a PLL or from ring **oscillators**, in order to **sample** one by the other. This

creates good TRNGs albeit ... cific element such as a PLL or an internal **oscillator**. .... An embedded true **random number** gen- erator for **FPGAs**. ...
www.tsi.enst.fr/publications/enst/inproceedings-2007-7431.pdf - Similar

Random Number Generation in VHDL - Page 2 - Application Forum at ...
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random number generation in **FPGAs**. I know she looked at distribution as well as randomness in her study. I don't have her conclusions or ...
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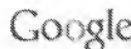
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delivery directly from the EPROM into the **FPGA**, with the CPLD managing the .... long to
create the pseudo-**random number**. A thousand (decimal) is a little ...
www.xilinx.com/support/documentation/white_papers/wp197.pdf

An **FPGA** Implementation of a Parallelized MT19937 Uniform **Random** ...
by S Vinay - 2009 - Related articles
While there have been previous attempts to accelerate the MT19937 pseudouniform
random number generator using **FPGAs** we believe that we can substantially ...
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by D Schellekens - Cited by 26 - Related articles
random numbers in a purely digital fashion; making it suit- able for any **FPGA** type,
because no **FPGA** vendor spe- cific features (e.g., like phase-locked ...
citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.66.5319&rep...

[PDF] **FPGA** Implementation of Neighborhood-of-Four Cellular Automata ...
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by B Shackleford - 2001 - Cited by 35 - Related articles
Nov 14, 2001 ... Figure 6: **FPGA** physical layouts and maximum predicted operating
frequency for the neighborhood-of-four cellular automata **random number** ...
www.hpl.hp.com/techreports/2001/HPL-2001-290.pdf

Seeding **random number** generator | Comp.Arch.**FPGA** | FPGARelated.com
4 posts - 1 author - Last post: May 7, 2009
Arch.**FPGA** usenet news group on FPGARelated.com. ... Does each process have its own
random number stream or do they share some global random ...
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hardware statistical tests, AIS 20, AIS 31, **FPGA**. I. INTRODUCTION. Producing high-
quality **random number** sequences is a difficult task, especially when high ...
renaudsantoro.web2host.co.cc/Files/Publi/ISSSE07.pdf

An embedded true **random number** generator for **FPGAs**
by P Kohlbrenner - 2004 - Cited by 48 - Related articles
Knut Wold , Chik How Tan, Analysis and enhancement of **random number** generator in
FPGA based on oscillator rings, International Journal of Reconfigurable ...

portal.acm.org/citation.cfm?id=968280.968292

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by S Banks - Cited by 1
FPGA based implementations of two classes of Pseudo. **Random Number**(PRN)
generator, intended for use in. Monte Carlo methods for finance, are presented. **FPGA ...**
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Implementations of our parallel Mersenne Twister **number** generator core on **Xilinx** Virtex4
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folk.uio.no/jimtoer/ices05.pdf

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random number generator (PRNG) derives its output from a physical noise source and its
output is ... design occupies approximately 130 **Xilinx** Virtex slices. ...
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FPGAworld
Do you really need a true **random number** or would a pseudo **random number** do? See
Xilinx app note 052 for LFSR-based pseudo-**random number** generators: ...
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plc, Celoxica Ltd. and **Xilinx** Inc. for supporting this work. References. [1] random.org A
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Paddy Dempster's **Random** Memories. Just another WordPress.com weblog ... There are a
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Nested digital phase lock loop
US Pat. 5463351 - Filed Sep 29, 1994 - Motorola, Inc.
Transition 601 is used as the first center **bit sample** location for **bit** 0, the
next center **bit sample** occurs at 605. The **rising edge** of the wideband ...

Method and apparatus for measuring the phase of captured read data
US Pat. 6509762 - Filed Aug 8, 2002 - LSI Logic Corporation
The synchronized, captured logic states, labeled **falling edge** of DQS), ... 55
gates each **bit** of CAPTURE2[1:0] independently with **rising edge** of **sample** clock
...

Input-output element has self timed interface using a received clock
signal ...
US Pat. 5513377 - Filed Jun 17, 1994 - International Business Machines Corporation
The **rising edge** data samples are clocked into the RES latches and the **falling
edge** data samples are clocked into the FES latches. The outputs of the RES-FES
...

System for recovering data in a multiprocessor system comprising a ...
US Pat. 6668335 - Filed Aug 31, 2000 - Hewlett-Packard Company, L.P.
This string of three samples preferably includes a **falling edge sample** and two
rising edge samples (on either side of the **falling edge sample**). ...

Multi-function timer with shared hardware
US Pat. 6334191 - Filed May 19, 2000 - Visteon Global Technologies, Inc.
The **edge** variable is a two-**bit** field that programs which **edge** on the pin line 60
qualifies as an input event, ie, the **rising edge**, **falling edge**, ...

Method and system for decoding data in a signal
US Pat. 6239843 - Filed May 5, 1997 - WAVO Corporation
10 25 **sample** and a **sample** that preceded the current **sample** by 24 samples, ...
Like the **rising edge** and maximum analyses, the **falling edge** and minimum ...

Timing calibration apparatus and method for a memory device
signaling system
US Pat. 6920540 - Filed Oct 22, 2002 - Rambus Inc.
The four pulses could be sampled by the **rising and falling edge** of CLKB on bus
... The resulting pattern of eight **sample** values will indicate where the **bit** ...

Method and apparatus for locating data transition regions
US Pat. 6690201 - Filed Jan 28, 2002 - Xilinx, Inc.
At step 402, **falling** samples are moved into the **rising sample** domain. ... if a
transition or "error" (E) is detected on a **rising** or a **falling edge** in ...

Field programmable radio frequency communications equipment
including a ...
US Pat. 6343207 - Filed Nov 3, 1998 - Harris Corporation
12, with abbreviated multi **bit** 5 commands from the control registers applied to
Selects **rising** or **falling edge sample** Selects the number of least

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Phase **Noise** in High-Performance **Oscillators** - impact of ultralow ...
In an **oscillator**, phase **noise** is the rapid **random** fluctuations in the phase .... adjacent-
channel interference and **bit** error rate (BER), to name a few. ...
mobiledevdesign.com/.../radio_impact_ultralow_phase/ - Cached - Similar

Fast physical **random bit** generation with chaotic semiconductor ...
by A Uchida - 2008 - Cited by 13 - Related articles
Nov 23, 2008 ... **Random** phenomena such as photon **noise**, thermal **noise** in resistors,
and frequency jitter in **oscillators** ... An example of **random bit** generation at the maximum
rate achievable with the lasers in this setup is shown in Fig. ...
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Method and apparatus to produce a **random bit** sequence - Patent 6362695
by MA Bailey - 2002 - Related articles
Low **noise oscillator**. Lockwood - - 4580109. **Random** binary number generator. Soenen
et al. ... and applying the first signal to **sample** a second signal. ...
www.freepatentsonline.com/6362695.html

Patents In Field 331/78 - **Oscillators** (ELECTRICAL **NOISE OR RANDOM** ...
A pseudo-**random bit** generator using at least one N-round ..... A Gaussian **noise**
generator having a **sample** and hold circuit connected to the output thereof ...
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Really **Random** Topics
Oct 31, 1995 ... David Battie: Use a low-frequency **oscillator** to **sample** a ... diode **noise** to
toggle a flip-flop, and **sample** at **random**. ... 1991-09-25 Mark Johnson: Get an even **bit**
distribution by XORing **random bits** with a square wave. ...
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Truly **random** number generators based on a non-autonomous chaotic ...
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Drift between the two **oscillators** provides **random bit** generation to be more ..... An
integrated analog/digital **random noise** source, IEEE Trans Circuits and ...
linkinghub.elsevier.com/retrieve/pii/S1434841106000720

A Leakage-Based **Random Bit** Generator with On-line Fault Detection
exploits the jitter in digital ring **oscillators** has been proposed in [3] where a feedback
strategy is adopted to .... 40pA (parameters in a 0.22μm CMOS **sample** design), a **bit** ... A
novel, patent pending, **noise** source for a **random bit** ...
doi.ieeecomputersociety.org/10.1109/DDECS.2006.1649623

Calculating the entropy - ComScire - The **Random** Number Generator ...
The entropy is derived from a **random noise** component in one or both of a low- ... If the
entropy content is less than one **bit** per **bit**, the sequence is not a true ... The distribution of
a crystal **oscillator** jitter is nearly Gaussian, ...
comscire.com/Products/PCQNG20/HCalc.aspx - Cached

A **noise**-based IC **random** number generator for applications in ...
ideally producing a **random bit** for each **sample**. Additionally, ... effects of input **noise** on
the **oscillator** output frequency, the os- ...
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1/f **frequency** fluctuations of a quartz **oscillator** and temperature ...
quency stability; even the atomic **clock** relies on a quartz **clock** as regards its short-term
stability. .... **oscillator** placed in a **double** oven and their coherence function. ... **frequency**
fluctuations (1)-**Random**- walk waveform- ...
doi.wiley.com/10.1002/ecja.4400670704

Section One
The "**frequency** stability" of an **oscillator** is a term used to ... Now, y(t) is a dimensionless
quantity and useful in describing **oscillator** and **clock** performance; eg., ... Typically, for
many **oscillators**, if the **sample** time is long compared to ... **oscillators** are fed into the two
ports of a **double** balanced mixer as ...
tf.nist.gov/phase/Properties/one.htm - Cached - Similar

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by F Herzel - 1999 - Cited by 190 - Related articles
jitter is a **double**-differential quantity in that three zero crossings of the .... In addition to
components at the **clock frequency** and ... exhibits **random** signals resulting from the
activities of each building block as well. ...
huangkejie.com/.../A%20Study%20of%20Oscillator%20Jitter%20Due%20to%20Supply%
20an...

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a pattern of known **frequency** (**clock** itself in this case) is sent at both data and **clock** lines.
.... dimension of flexibility in **random** clock **oscillator** design. ... chosen because in a
typical **double** data rate digital signaling ... phase difference for any given **sample** size and
confidence level. Fig. ...
citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.73.2679&rep...

[PDF] A study of phase noise in CMOS **oscillators**
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by B Razavi - 1996 - Cited by 491 - Related articles
VOLTAGE-CONTROLLED **oscillators** (VCO's) are an integral part of phase-locked loops,
clock recovery cir- cuits, and **frequency** synthesizers. **Random** ...
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Sample And Hold With VC-**Clock** (+/-12V to +/-15V)
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patterns that emerge as you vary the **sample frequency** or the **frequency** of the sampled
signal. When noise is sampled then **random** non-repeating patterns are output. ... Paolo's
S&H modulating a Sine **oscillator** (w/Portamento). ...
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Clock Terminology
The output **frequency** of a crystal **oscillator** varies with temperature and voltage. ...
Double Oven **Oscillators** contain two ovens, with the ... **Clock** Jitter Figure 3. Period Jitter
Random thermal noise from crystal, ...
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A CMOS **Clock** and Data Recovery Circuit with a Half-Rate Three ...
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half-rate **clock** is developed to the **random** data stream. The proposed phase detector has the same operation as the con- ... difficult to design a very high-speed **oscillator** that provides ... **clock** edges **sample** the data waveforms. The error is then .... in a 0.35-µm **double**-poly N-well CMOS technology. Fig- ...
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[0027]This **random oscillator frequency**, fr, drives the digital logic of the .... **frequency double** the host Primary **Clock frequency**; see the **Sample** 320 bit ...
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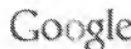
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Beside the internal **oscillator** clock, timer status can also be increased by ... 1 = **rising**
edge 0 = **falling edge**. bit 5 T0CS (TMR0 Clock Source Select bit) ...
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falling edge of clock to **sample** data bit at centre
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I thought we need to oversample the **oscillator** to about 2 times the frequency of the ...
sample data at **rising**/**falling edge** of clock? (1) ...
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Divide-by-X.5 circuit with frequency doubler and differential ...
The high-frequency output signal VCOUT of voltage-controlled **oscillator** VCO 40 is ... The
first **rising** edge of CLKIN clocks first and second flip-flops 61, 62. ... The first **falling edge**
of the VCO clock input (CLKIN) clocks third ...
www.patentstorm.us/patents/6882229/description.html

Jitter and Phase Noise in Ring **Oscillators**
The Impulse Sensitivity Function for Ring **Oscillators**. The period consists ofN **rising**-edge
and N **falling-edge** delays, i.e.,. Combing (5.5) and (5.7) results ...
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Ring **oscillator** - US7659786
The present invention relates to a ring **oscillator** which is capable of measuring **rising and
falling** edge propagation delays independently. ...
www.patents.com/Ring-oscillator/US7659786/en-US/ - Cached

Process monitor with statistically selected ring **oscillator** ...
by R Bach - 2003 - Cited by 2 - Related articles
The ring **oscillator** 104 may be made, for example, by connecting a delay chain of .... a
rising edge reference delay TR REF 510 , a **falling edge** reference delay TF .... The M
input 762 is then set to logic "0", and the **rising** edge of a ...
www.freepatentsonline.com/6544807.html

(WO/2007/069138) NO EN TITLE
Thus, the fourth clock signal 24 has a **rising** edge at t=3T/4 and a **falling edge** at t=T/4. As
a result, the ring **oscillator** 1 of Fig. ...
www.wipo.int/pctdb/ja/wo.jsp?WO=2007069138&IA... - Cached

Relaxation **oscillator** - Wikipedia, the free encyclopedia
[edit] Practical examples of the use of the relaxation **oscillator** ... the **rising** edge as the
flash capacitor is charged and the rapid **falling edge** as the ...
en.wikipedia.org/wiki/Relaxation_oscillator - Cached - Similar

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CLK signal of the ring **oscillator**. ...
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PHASE NOISE MINIMIZED PHASE/FREQUENCY-LOCKED VOLTAGE-CONTROLLED ...
Rising and Falling Edge-Rates Equalization [0037]It has been shown that symmetry in ring

oscillator significantly affects the up-conversion of flicker noise ...
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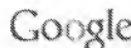
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A **random number** generator having a digital ring **oscillator** comprising:a ... Edges of the
slow **clock** signal 112 comprise a jitter. The **sample** signal ... Conversely, when inputs A
and B are equal, inverter 300 **inverts** the input signal. ...
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Clock system with internal monitor circuitry for secure testing ...
In the **random number** generator circuit 100, one or more ring **oscillators** are .... **inverted**
by inverter 406, is connected to the **clock** input of the D-type ...
www.patentstorm.us/patents/5633609/description.html

High speed true **random number** generator based on open loop ...
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Pseudo **Random Number** Generators (prng) can suffice for most applications but ... The
simplest method to generate trng in a cmos circuit is to **sample** the thermal noise. ... The
oscillator structures are easily implementable in FPGAs. .... The trng output is
resynchronized by a dff with **inverted** global **clock** as the ...
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Clock oscillators are circuits which generate square wave or ..... White noiseis pure
random noise, and the pink noise is specifically filtered whitenoise. ... open timing windows,
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Keywords: **Random number** generation, ring **oscillators**, generalized ... For example, the
input and **clock** signals for a D-type ..... at each 0-1 transition in the **oscillator** signal and
then **sample** the state when a random bit is needed. ...
islab.oregonstate.edu/koc/ches proc/4727/47270045.pdf

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be shown that the binary data S(dual **oscillator**)i is the **inverse** of least significant bit of the
ratio ... quency and the duty cycle of the fast **clock**, respectively. In .... **oscillator**-based
random number generators was developed. ...
www.eurasip.org/Proceedings/Eusipco/Eusipco2008/.../1569105011.pdf

Random Number Machines: A Literature Survey
Dec 4, 2002 ... The **oscillator** output **clocks** a 8-bit binary counter, which is the digital
output ..... Flipflop B, which gives the output, is **inverted** whenever A turns on ... Why does
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signal to **sample** the value of a second **clock** signal on each cycle. If the two **clock**
frequencies are ..... **oscillator**-based **random number** generators, IEEE ...
citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.86.2324&rep...

Ring **oscillators** problem - Old ISE Board -- no new thread - Xilinx **...**
3 posts - 1 author - Last post: May 9, 2008
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flip-flop, these errors appear again. **...**
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running **oscillator**, **clock** jitter, astable multivibrator and etc. **...** the exact **sample** values,
ideally producing a **random** bit for each **sample**. **...**
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